

FORM PTO/SB/08A/B (10-01)  
Substitute for PTO-1449A/B

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Attorney Docket Number

45313/DMC/V165

Application Number

10/020,491

Filing Date

December 1, 2004

Applicant(s)

Chuong

Group Art Unit

2663

Examiner Name

Chau T. Nguyen D. Fern's

**U.S. PATENT DOCUMENTS**

EXAMINER INITIALS	Cite No. <sup>1</sup>	DOCUMENT NUMBER Number - Kind Code <sup>2</sup> (If Known)	PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE
Dut	-	5,355,372	10-11-1994	Sengupta et al.
Dr	-	5,367,520	11-22-1994	Cordell


**FOREIGN PATENT DOCUMENTS**

EXAMINER INITIALS	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (If Known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T <sup>6</sup> (✓)

**OTHER DOCUMENTS**

EXAMINER INITIALS	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
Dut	-	Fan, et al., "Expandable ATOM Switch Architecture (XATOM) for ATM LANs", Supercomm/ICC., New Orleans, International Conference on Communications (ICC), New York, IEEE, U.S., Vol. 1, 1 May 1994 (p. 402-409).
Dut	-	Kumar, et al. "Performance Enhancement In Buffered Delta Networks Using Crossbar Switches And Multiple Links", Journal of Parallel And Distributed Computing, Academic Press, Duluth, MN, US, Vol. 1, No. 1, August 1984 (p. 81-103).
Dr	-	Chowdhury, et al., "Design And Performance Of A Threshold-Based Load Balancing Scheme In An ATM Switch", Computer Networks and ISDN Systems, North Holland Publishing, Amsterdam, NL, Vol. 29, No. 2, 1997 (p. 157-164).
Dr	-	European Search Report for European Application No. 02090163.3, filed May 3, 2002, Search Report completed 24 November 2003 and mailed 16 December 2003 (5 pages total).

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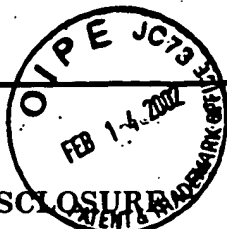
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FORM PTO-1449

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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Attorney Docket Number	45313/PYI/V165
Application Number	10/020,491
Filing Date	December 11, 2001
Applicant(s)	Chuong Vu
Group Art Unit	Unknown 2663
Examiner Name	To Be Assigned D. Fern's

**U.S. PATENT DOCUMENTS**

EXAMINER INITIALS	DOCUMENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

**FOREIGN PATENT DOCUMENTS**

EXAMINER INITIALS	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

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**OTHER DOCUMENTS**

EXAMINER INITIALS	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
<i>Dr</i>	Wikander, "A Multi-Chip Architecture for High-Capacity Packet Switching", Master Thesis, Luleå University of Technology, December 30, 1999 (pp 1-61)
<i>Dr</i>	Buhrgard, et al., "A scaleable ATM switching solution", Ericsson Review, No. 1, 1998 (pp 24-31)
<i>Dr</i>	Piekarski, "Reinventing the Switch Fabric Architecture", URL: <a href="http://www.commsdesign.com/story/OEG20010521S0113">http://www.commsdesign.com/story/OEG20010521S0113</a> , Communications Systems Design, June 1, 2001 (6 pgs)
<i>Dr</i>	IBM Microelectronics, "The Network Processor Enabling Technology for High-Performance Networking", Network Processor, 1999, (5 pgs) <a href="http://www.chips.ibm.com">http://www.chips.ibm.com</a>

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